

GLSVLSI 2026

June 22nd - June 24th 2026, Finger Lakes, NY, USA

<http://www.glsvlsi.org/>

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Call for Late Breaking Research Papers: 2-page limit

Original, unpublished late breaking research papers describing preliminary research in the general areas of VLSI, EDA and system design are solicited. Please visit <http://www.glsvlsi.org/> for more information.

Program Tracks:

- **VLSI Circuits and Design:** ASIC and FPGA design, microprocessors/micro-architectures, embedded processors, high-speed/low-power circuits, analog/digital/mixed-signal circuits, NoC, SoC, IO & interconnects, memories, lab-on-a-chip, biomedical sensors and devices, circuits for wearable or robotic devices, VLSI design and optimization.
- **VLSI for Artificial Intelligence (AI):** hardware AI accelerators, novel circuit and architectures for machine learning & AI, brain-inspired and neuromorphic computing hardware, compute-in-memory design.
- **IoT and Smart Systems:** circuits, computing, processing, and design of IoT and smart systems such as smart cities, smart healthcare, smart transportation, smart grid etc.; cyber-physical systems; machine learning for IoT; robots; wearable device and system; network & communication system.
- **Computer-Aided Design (CAD):** hardware/software co-design, high-level synthesis, logic synthesis, simulation and formal verification, layout, design for manufacturing, algorithms and complexity analysis, physical design (placement, route, CTS), timing analysis, signal and power integrity, machine learning and AI for CAD and EDA.
- **Testing, Reliability, Fault-Tolerance:** digital/analog/mixed-signal testing, reliability, robustness, static/dynamic defect- and fault-recoverability, variation-resilient design, learning-assisted testing.
- **Emerging Computing & Post-CMOS Technologies:** nanotechnology, quantum computing, approximate and stochastic computing, post CMOS VLSI.
- **Hardware Security:** trusted IC, IP protection, hardware security primitives, reverse engineering, side-channel attack/defense, CPS/IoT security, AI for HW security, hardware for classical and post-quantum cryptography.

Microelectronic System Education Workshop:

A one-day co-located workshop will cover the following topics: pedagogical innovations using a wide range of technologies such as ASIC, FPGA, multicore, GPU, TPU, educational techniques including novel curricula and laboratories, assessment methods, distance learning, textbooks, and design projects, industry and academic collaborative programs and teaching.

Paper submission deadline:

March 31st, 2026 (11:59 pm EST); Acceptance Notification: April 30th, 2026

Camera-Ready:

May 15th, 2026

Paper Submission: Authors are invited to submit full-length 6-page, original, unpublished papers along with an abstract of at most 200 words. Submissions exceeding 6 pages are permitted but must not exceed 8 pages in total. Each additional page beyond 6 will incur an extra fee upon acceptance for publication. Reference is counted toward the page limit. To enable blind review, the author list should be omitted from the main document. Previously published papers or papers currently under review for other conferences/journals should NOT be submitted and will not be considered. Electronic submission in PDF format to the <https://easychair.org/conferences/?conf=glsvlsi26> website is required. Author and contact information (name, affiliation, mailing address, telephone, fax, e-mail) must be entered during the submission process.

Paper Format: Submissions should be in camera-ready two-column format, following the ACM proceedings specifications located at: [ACM Template](#) and the classification system detailed at: [ACM 2012 Class](#). For Overleaf users, please find the following template: [ACM Proceedings Template - Overleaf](#). For LaTeX users, please find the following ZIP file: [acmart-primary.zip](#). For Word users, please find the following template: [interim-layout.docx](#).

Paper Publication and Presenter Registration: Papers will be accepted for regular or poster presentation. Every accepted paper MUST have at least one author registered to the symposium by the time the camera-ready paper is submitted; at least one of the authors is also expected to attend the symposium and present the paper.

ACM Open Access and APCs: As from January 1, 2026, all ACM Publications will be published on a fully Open Access basis and will require participation in [ACM Open, the payment of an APC, or a geographic or discretionary waiver to publish with ACM](#). Authors from institutions not participating in ACM Open will need to pay an APC to publish their papers, unless they qualify for [a financial waiver](#).

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